



Gabara 91-2-36

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): T.J. Gabara et al.
Case: 91-2-36
Serial No.: 10/719,193
Filing Date: November 21, 2003
Group: To Be Assigned
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature:

Lisa L. Vulpis

Date: December 26, 2003

Title: Integrated Circuit with Controllable Test Access
to Internal Analog Signal Pads of an Area Array

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicants submit herewith six (6) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

Joseph B. Ryan

Date: December 26, 2003

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